



CERTIFICATE OF MAIL

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231, on December 19, 2002.

Jinny Nguyen
Jinny Nguyen

RECEIVED
JAN -3 2003

TECHNOLOGY CENTER 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: December 19, 2002

~~Mark~~ RANDOLPH

~~Serial~~ No.: 10/032,646

Group Art Unit: 2811

~~Filed~~: December 27, 2001

Examiner: Loke, Steven Ho Yin

~~For~~: PLANAR TRANSISTOR STRUCTURE USING ISOLATION IMPLANTS FOR IMPROVED VSS RESISTANCE AND FOR PROCESS SIMPLIFICATION

#3/Election
P.T.
1/10/03

Assistant Commissioner of Patents
Washington, D.C. 20231

RESPONSE

In response to the Office Action dated December 12, 2002 please amend the above-identified application in the following manner:

REMARKS

This Response is in response to the Office Action dated December 12, 2002. Claims 1-10 are pending in the present application.

In the above-mentioned Office Action, the Examiner stated that claims 1-3, and 4-10 are subject to a restriction requirement under 35 U.S.C. § 121. In particular, the Examiner stated that